A Report on

One-Day National Level Workshop

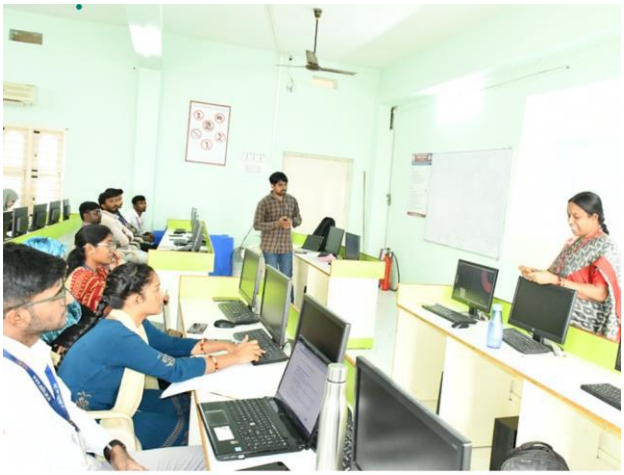
"LUT-Based Device Modeling for Advanced Circuit Design: A Cadence Workflow"

Organized by Department of Electronics & Communication Engineering in association with MITS – ISTE Date: 06.01.2026

Report submitted by: Dr. G. Naga Jyothi, Associate Professor, Department of Electronics & Communication Engineering.
Resource Person Details: Dr. Thoti Narasimhulu, Scientific Researcher, University of Oulu, Finland.
Report Received on 20.01.2026.
Total No. of Registrations: 35
Mode of Conduct: Offli

About the Workshop:

The One-Day National Level Workshop titled "LUT- Based Device Modeling for Advanced Circuit Design: A Cadence Workflow" was organized by the Department of Electronics and Communication Engineering in association with the ISTE Student Chapter. The workshop focused on providing participants with comprehensive knowledge of lookup table (LUT) based device modeling techniques and their application in advanced circuit design using Cadence tools. The program aimed to enhance technical expertise, promote industry-relevant skills, and bridge the gap between theoretical concepts and practical implementation in modern electronic design automation workflows.



2 of 15

Inaugural Session:

The programme started at 6-Jan-2026 at 10.00 am with a presidential address and a welcome address by Dr. S. Rajasekaran, HoD & Professor, and Department of ECE. In the welcome address, Dr. S. Rajasekaran highlighted the participant's summary from states of India.

Resource Person: Dr. Thoti Narasimhulu, Scientific Researcher, University of Oulu, Finland.

The technical session was handled by Dr. Narasimhulu Thoti, Scientific Researcher, University of Oulu, Finland. The resource person provided an in-depth explanation of LUT-based device modeling techniques and highlighted their advantages over traditional modeling approaches. The session included practical demonstrations of Cadence workflows for advanced circuit design and simulation. The participants actively interacted during the session, making it highly engaging and informative.

Participation:

Faculty members, research scholars, and students from the Department of ECE actively participated in the workshop. The interactive nature of the session facilitated effective knowledge transfer and skill enhancement.

Outcome of the Workshop

1. Participants gained a strong understanding of LUT-based device modeling techniques.
2. Enhanced awareness of Cadence tools and workflows used in advanced circuit design.
3. Improved technical skills relevant to research and industry applications.
4. Encouraged students towards research-oriented learning and innovation.
5. Strengthened industry–academia interaction.

Conclusion:

The one-day national level workshop was successfully conducted and achieved its intended objectives. The Department of ECE expressed its sincere gratitude to the management, resource person, faculty members, and participants for their support and cooperation in making the workshop a grand success.

Newspaper Clips:

మిట్స్ ఐ.ఎస్.టి.ఈచే ఒక్కరోజు జాతీయస్థాయి వర్కుషాప్

హాజరైన శాస్త్రీయ పరిశోధకులు తోటి డాక్టర్ నరసింహులు

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Feedback Form: